

**LIQUID CRYSTAL DISPLAY HAVING GATE DRIVING SIGNAL
LINE IN PANEL AND CORRECTION CIRCUIT**

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BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates generally to a liquid crystal display and, more particularly, to a 10 liquid crystal display having a signal line for driving a gate driver IC in panel and a correction circuit for correcting gate driving power supply.

2. Description of the Prior Art

15 As well known, a liquid crystal display generally includes a driving circuit unit for driving a liquid crystal panel.

Referring to Fig. 1A, a conventional liquid crystal display comprises a X-PCB 2 having a column 20 driver (that is, data driver IC) for supplying a data signal to a panel 1 thereon, a Y-PCB 4 having a row driver (that is, gate driver IC) for driving scan line (that is, gate electrodes in panel) thereon and a flexible Printed Circuit (FPC) 6 for connecting

each PCB.

Thereafter, a method has been proposed that the FPC (6 of Fig. 1 A) is removed, as shown in Fig. 1B. And, another method has been proposed that the Y-PCB 5 (4 of Figs. 1A and 1B) is removed and signals for driving gate driver IC are applied through signal line in the panel 1.

However, when the driving signal line of gate driver IC is formed in the panel 1, there are 10 problems of voltage drop and skew of analog power supply.

Referring to Fig. 2, a plurality of gate driver IC 8 are formed on one side of panel 1 and the gate driver IC 8 is operated by driving signal line formed 15 in the panel 1. In this case, analog power supply applied to the gate driver IC 8 has problems of voltage drop and voltage skew due to resistance R1, R2 and R3 in panel 1. According to a conventional method, analog power supply applied to the gate 20 driver IC generates gate on/off currents by using DC/DC converter and charge pump circuit.

Therefore, voltage applied to a first gate driver IC has a problem of voltage drop corresponding to multiplied value of resistance R1 in panel and

current of power supply terminal I_{rms} and a problem of voltage skew corresponding to multiplied value of resistance $R1$ in panel and inrush current of power supply terminal I_{rush} , thereby forming a waveform of 5 A in Fig. 2.

Similarly, voltage applied to a second gate driver IC has a problem of voltage drop corresponding to multiplied value of resistance $R1+R2$ in panel and current of power supply terminal I_{rms} and a problem 10 of voltage skew corresponding to multiplied value of resistance $R1+R2$ in panel and inrush current of power supply terminal I_{rush} , thereby forming a waveform of B in Fig. 2.

As mentioned above, voltage with different values 15 is applied to each gate driver IC. In particular, gate turn-on voltage and turn-off voltage have direct influences on charge properties of liquid crystal display, thereby generating block and flicker between each gate driver IC.

20 That is, when signals are transmitted through PCB or FPC, voltage is supplied without skew of input power supply. However, when signals are applied to the gate driver IC 8 through the panel 1 without using PCB or FPC, skew is generated in signal,

thereby lowering the screen quality.

In particular, when a gate pulse starts operation, current is instantaneously increased, thereby causing sudden voltage drop and voltage skew.

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SUMMARY OF THE INVENTION

Therefore, the present invention has been made to solve the above problems and an object of the present 10 invention is to provide a liquid crystal display having remarkably improved screen quality by regularly supplying current applied to gate driver IC to reduce voltage drop and by correcting voltage skew to reduce voltage difference between gate driver IC, 15 when a signal line is formed in panel to drive gate driver IC.

In order to accomplish the above object, the present invention comprises a gate driving power supply unit for supplying analog signal, a control 20 circuit unit for applying control signal to analog signal outputted from the gate driving power supply unit; a correction circuit unit for applying control signal from the control circuit unit to correct analog signal outputted from the gate driving power

supply unit into saw type; and a corrected power supply unit for supplying saw type signal corrected in the correction circuit unit to the gate driver IC.

According to the present invention, it is
5 desirable that the saw type signal has the same cycle
as gate pulse width and the correction circuit unit
includes OP Amp or transistor. And, the gate driving
power supply unit may include DC/DC converter having
OP Amp or transistor. The OP Amp or transistor is
10 formed only on gate turn-on line or both on gate
turn-on line and gate turn-off line.

BRIEF DESCRIPTION OF THE DRAWINGS

15 Figs. 1A to 1C are drawings for showing a
structure of general panel driving circuit unit.

Fig. 2 is a drawing for showing a waveform of
voltage applied to gate driver IC through a signal
line in panel according to a conventional method.

20 Fig. 3 is a block diagram for showing a gate
driving circuit unit including saw type correction
circuit according to the present invention.

Fig. 4 is a drawing for showing a waveform of
input/output voltage of gate driver IC through the

correction circuit of Fig. 3.

DETAILED DESCRIPTION OF THE INVENTION

5 The above objects, and other features and advantages of the present invention will become more apparent after reading the following detailed description when taken in conjunction with the appended drawings.

10 According to the present invention, current applied to gate driver IC is regularly supplied in order to stably supply driving current of gate electrodes in panel (that is, scan line), thereby reducing voltage drop and at the same time, current 15 is supplied in a saw type, thereby correcting voltage skew. Therefore, it is possible to minimize voltage difference between each gate driver IC.

Fig. 3 is a block diagram for showing a gate driving circuit unit including a saw type correction 20 circuit according to the present invention. Referring to Fig. 3, the gate driving circuit unit comprises a gate driving power supply unit 10 for supplying analog signal, a control circuit unit 12 for applying control signal to analog signal outputted from the

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gate driving power supply unit 10, a correction circuit unit 14 for applying control signal from the control circuit unit 12 and correcting analog signal outputted from the gate driving power supply unit 10 5 into a saw type, and a corrected power supply unit 16 for supplying saw type signal corrected in the correction circuit unit 14 to gate driver IC.

The saw type correction circuit unit 14 has a transistor (not shown) or OP-Amp (not shown) therein 10 and a plurality of resistance and capacitors are connected to the OP Amp, thereby converting the output waveform of OP Amp into a desirable saw type. That is, the correction circuit unit 14 converts 15 voltage waveform into a desirable saw type before current is applied to the corrected power supply unit 16 to drive gate electrodes in panel through gate driver IC.

Fig. 4 is a drawing for showing a waveform of input/output voltage of gate driver IC through the 20 correction circuit of Fig. 3. As shown in Fig. 4, input waveform of gate driver IC is converted into saw type through the correction circuit unit 14 of Fig. 3. Therefore, it is possible to minimize voltage difference in output waveform of each gate drive IC.

According to conventional method, voltage difference is generated between each gate driver IC, thereby causing voltage drop of analog power supply. However, according to the present invention, power is supplied in a saw type to correct voltage skew, thereby removing difference between each gate driver IC. Therefore, it is possible to generate the same voltage output. And, according to the present invention, the input voltage waveform of saw type has the same cycle as gate pulse width applied to gate electrode.

According to a preferred embodiment of the present invention, the OP Amp or transistor of the correction circuit unit is formed only on gate turn-on line or both on gate turn-on line and gate turn-off line.

And, according to another embodiment of the present invention, the gate driving power supply unit includes DC/DC converter having OP Amp or transistor. The OP Amp or transistor is also formed only on gate turn-on line or both on gate turn-on line and gate turn-off line.

As described above, according to the present invention, although a signal line is formed in panel to drive gate driver IC, current applied to gate

driver IC is regularly supplied to reduce voltage drop and at the same time, current is supplied in a saw type to correct voltage skew, thereby minimizing voltage difference between each gate driver IC. As a 5 result, it is possible to stably supply gate electrode driving current and remarkably improve screen quality of liquid crystal display and moreover, ensure reliability of product.